

DUAL FUNCTION BUSY PIN

ABSTRACT OF THE DISCLOSURE

5

Provided is a method and apparatus for multiplexing a busy signal and another signal via a dual function pin. Specifically, a multiplexer in a display controller can select a wait signal or the busy signal and generate a signal to a processor. The signal to the processor can indicate the availability of a module in the display controller, such as JPEG module, for access by the processor. If the module is not available for access, then the processor continuously processes tasks without attempting to access the module in the display controller. The dual function pin, by interfacing with the busy signal, obviates an attempt by the processor to determine the availability of the module and permits backward compatibility with processors that can interface with another signal.

10

15